

A decorative graphic at the top of the page consisting of two overlapping wave-like patterns of dots. The upper wave is composed of green dots, and the lower wave is composed of blue dots. Both waves start on the left and end on the right, with the green wave being slightly higher and further to the right.

# CP4400 Release Notes

Revision: 2.8.70 (5486)

2019-12-18

# 1 Overview

This document contains the SW revision history for CP4400 TS Processor. The release note is cumulative, each chapter is describing changes since the previous released version.

## 2 Upgrade instructions

- Software upgrade of the device should – if possible – be done at a time when the device is not in use for critical signal transmission.
- Always take a backup of the device's current configuration before doing the upgrade (use Device Info > Save/Load Config > Save Config in the device's GUI).
- Have a copy of the original software file currently in use at hand, in case of a need for reverting to the last operational state.

### 2.1 How to upgrade

- Log into the unit as admin using any web browser. Navigate to Device Info > Maintenance > SW Upgrade and click on Browse. Then upload the software image (\*.bin) from your file system. Make sure Slot M: Main Board is checked and hit the Upgrade button.
- After the software is loaded the unit will reboot and load the new software image. Loading the new software image takes the same amount of time as a normal reboot, which may take from 45-90 seconds.
- It is possible to upload the software to the unit, and postpone the reboot operation. The old software will be used until the next reboot. This is done by disabling the Reboot after upgrade option on the Software Upgrade page.

## 3 Release 2.8.70

### 3.1 Release date 2019-12-18

- Deterministic Multiplexer: Fixed case where mux could hang after bit errors on APCR packets
- Seamless switch: Improved time to lock and performance under heavy error conditions

## 4 Release 2.8.60

### 4.1 Release date 2019-10-30

- Added support for T2-MI analysis. Requires T2AX licence.
- Added support for T2-MI PLP analysis. Requires PLPEXT licence.
- Fixed rare crash on close of Telnet session

## 5 Release 2.8.50

### 5.1 Release date 2019-07-04

Patch release to fix a few issues.

### 5.2 Stability

- Fixed an issue that could cause data processing to freeze right after a re-boot.
- Fixed an issue with loading of SW on slow links.

### 5.3 DVB-T2

- Fixed frame index calculation in Multi-Frame interleaving if P\_I != T2 frames per super-frame.
- Fixed issue where PLP data could disappear if changing PLP mode.
- Added option to specify Type2 sub-slices manually

## 6 Release 2.8.42

This release adds several new features to the CP4400 TS Processor.

### 6.1 Release date 2019-02-14

- Link Layer Discovery Protocol (LLDP) used on the management interfaces to simplify the network configuration. The CP4400 TS Processor. can advertise its network interfaces to the IP network and the network orchestration system
- Support for UDP only TS-IP output used in some distribution infrastructures and required to interface with some legacy equipment. Encapsulation of Transport Streams in IP frames without RTP protocol encapsulation
- Redesigned alarm page for simpler overview and better event representation. The new alarm page highlights the recent events that have not yet been acknowledged by the user. This allows for rapid identification of issues that need immediate attention
- GPS accuracy improvement for synchronization of the units including alarm logs and unit time. This version adds diagnostic and info to the GPS module. Time configuration of the unit will be done automatically removing the need for manual adjustment of the unit time
- Enhancements and fixes for improved performance and stability

## 7 Release 2.0.22

### 7.1 Release date 2017-08-30

- Fixed occasional problems booting if having a display connected or extension boards.
- Fixed issue when deleting an Input Switch.



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## 8 Release 2.0.14

### 8.1 Release date 2017-05-16

#### 8.1.1 New features

- Added DVB-T and DVB-T2 Seamless SFN switching capabilities to the seamless switch. Requires SSFNX licence.
- Added “Clock reference” page.
- Users may now add more than 1 seamless switch if licenced.
- Added support for DVB-T/T2 RF card.

#### 8.1.2 Bug fixes and enhancements

- Fixed issue where disabled input might be selected in input switch.
- Fixed issue where T2-MI PCR could jitter after boot.
- Fixed issue with false CC errors when discontinuity bit set.
- Other fixes and improvements.

#### 8.1.3 Known issues

- Using ASI output with a combination of 188 and 204 bytes packets from the same source might cause CC errors on the output.

## 9 Release 1.10.20

### 9.1 Release date 2016-09-16

#### 9.1.1 New features

- Added seamless switch functionality. Currently limited to a single 4:1 switch.
- Added local insertion functionality to deterministic multiplexer.
- Added support for DVB-T2 MIP insertion.
- Added support for T2-MI stream ID field.
- Added option to store configurations on the unit. Configuration files can be stored on the unit and activated by the user on the GUI or remotely via an NMS system (TXP, SNMP).
- SNMP: The NeoData-MIB is obsolete (but still supported) and replaced with NeoData2-MIB using only 16 bit values in the OIDs. This change has been introduced to ensure compatibility with MIB browser and other SNMP systems that do not support the 32 bit OIDs.
- Several other minor improvements.

#### 9.1.2 Bug fixes and enhancements

- T2-Gateway: Fixed issue with Type2 PLPs not setting slice interval correctly.
- Fixed issue where IP input could fail to start if uptime over 49.7 days
- NX-HW-S/S2-DEMODO-X2 DVB-S2 daughter board
  - Fixed calculation of transponder frequency when configured with C-band LNB.
  - Fixed an issue with setting of PL scrambling mode on boot.
  - Improved calculation of MER



**Caution:** If you have a DVB-S2 tuner, and you do not use PL scrambling, you might need to change the PL scrambling mode field to DVB-S2 Gold Code and set Gold Code/PRBS X root to 0. If not the tuner may not lock

## 10 Release 1.8.8

This release adds several new features and many stability improvements.

### 10.1 Release date 2016-01-27

- Added support for configurable Leap Seconds update in DVB-T2.
- The SFP ports may now be used for data traffic.
- Added support for ASI outputs on the main board
- Added full SNMP v1 and v2 support
- Added IP Ping.
- Added BISS1 descrambler. Licensed feature.
- Added Input Switch. Licensed feature.
- Added Deterministic Remultiplexing with deterministic T2-MI generation. Licenced feature.

### 10.2 Bugfixes

- Fixed an issue where T2-Gateway could not work correct for Seamless SFN.
- Several minor improvements and stability fixes.

# 11 Release 1.4.2

## 11.1 Release date 2015-04-13

This release adds support for IP-RX FEC.

## 11.2 Known limitations

- IP-RX channel struggles to lock initially during heavy packet loss conditions.
- ASI output not supported

## 12 Release 1.2.0

### 12.1 Release date 2015-03-25

Initial release with support for

- Up to 7 individual T2-Gateways
- Multi-PLP, up to 8 per T2-Gateway
- IP-RX and IP-TX
- IP-TX FEC
- RIPv2
- ASI and DVB-S/S2 extension boards

### 12.2 Known limitations

- IP-RX FEC not supported
- ASI output not supported